



中显液晶
技术资料



型号 ZX320240B-1

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1. GENERAL SPECIFICATIONS :

1-1 SCOPE:

This specification covers the delivery requirements for the liquid crystal display delivered by YAOYU TECHNOLOGY to Customers .

1-2 PRODUCTS:

Liquid Crystal Display Module (LCM)

1-3 MODULE NAME:

X320240B-1DBSWWD

2. FEATURES :

Item	Standard Value
Display Type	320 *240 dots
LCD Type	<input type="checkbox"/> FSTN, BLUE,Transmissive,Negative,Extened TEMP <input type="checkbox"/> FSTN, Transflective,Positive,Extened TEMP <input checked="" type="checkbox"/> STN, BLUE,Transflective,Negative,Extened TEMP <input type="checkbox"/> STN, GREY,Transflective,Positive,Extened TEMP <input type="checkbox"/> STN, Yellow-GREEN,Positive,Extended TEMP
Driver Condition	LCD Module: 1/240 Duty, 1/17Bias
Viewing Direction	6 O'clock
Backlight Type	<input type="checkbox"/> YELLOW-GREEN LED BL <input checked="" type="checkbox"/> WHITE EDGE LED BL <input type="checkbox"/> CCFL WHITE BL
Weight	TBD
Interface	8-bit 6800/8080 MPU interface
Driver IC	Driver IC: RA8802

3. MACHANICAL SPECIFICATIONS :

ITEM	SPECIFICATIONS	UNIT
OUTLINE DIMEMSIONS	139(L) X100(W) X 13.2max(H)	mm
VIEWING AREA	103(L) x 79(W)	mm

ACTIVE AREA	95.97(L) X 71.97(W)	mm
DISP.CONSTRUCTION	320 * 240 dots	---
DOT SIZE	0.27(L) x0.27(W)	mm
DOT PITCH	0.30(L) x0.30(W)	mm
ASSY.TYPE	COB	---
WEIGHT	TBD	g

Note : For detailed information please refer to LCM drawing

4. ABSOLUTE MAXIMUM RATING

ITEM	SYMBOL	CONDITION	STANDARD VALUE			UNIT
			MIN	TYP	MAX	
POWER SUPPLY FOR LOGIC	VDD	Ta=25°C	-0.3	—	4.0	V
INPUT VOLTAGE	VIN	Ta=25°C	-0.3	—	VDD+0.3	V
OPERATION TEMPERATURE	TOPR	---	-20	—	+70	°C
STORAGE TEMPERATURE	TSTG	---	-30	—	+80	°C
Storage Humidity	H _D	Ta < 40 °C	-		90	%RH

NOTES:

(1) LCM should be grounded during handling LCM.

5. ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITIONS	STANDARD VALUE			UNIT
			MIN	TYP	MAX	
POWER SUPPLY VOLTAGE	VDD—VSS	Ta= +25°C	2.7	3.3	4	V
POWER SUPPLY FOR LCD DRIVING	V _{lcd}	Ta= +25°C	20	-	25	V
INPUT VOLTAGE "H" LEVEL	V _{IH}	—	0.8VDD	—	VDD	V
INPUT VOLTAGE "L" LEVEL	V _{IL}	—	VSS	—	0.2VDD	V
OUTPUT VOLTAGE "H" LEVEL	V _{OH}	I _{OH} =-0.5mA	0.8VDD	—	VDD	V
OUTPUT VOLTAGE "L" LEVEL	V _{OL}	I _{OL} =0.5mA	VSS	—	0.2VDD	V
Supply Current	I _{DD}	V _{DD} = 5.0 V	-	3	5	mA
Supply Voltage (LED)	V-b1		2.9	3.1	3.3	V

Supply Current (CCFL)

I-bl

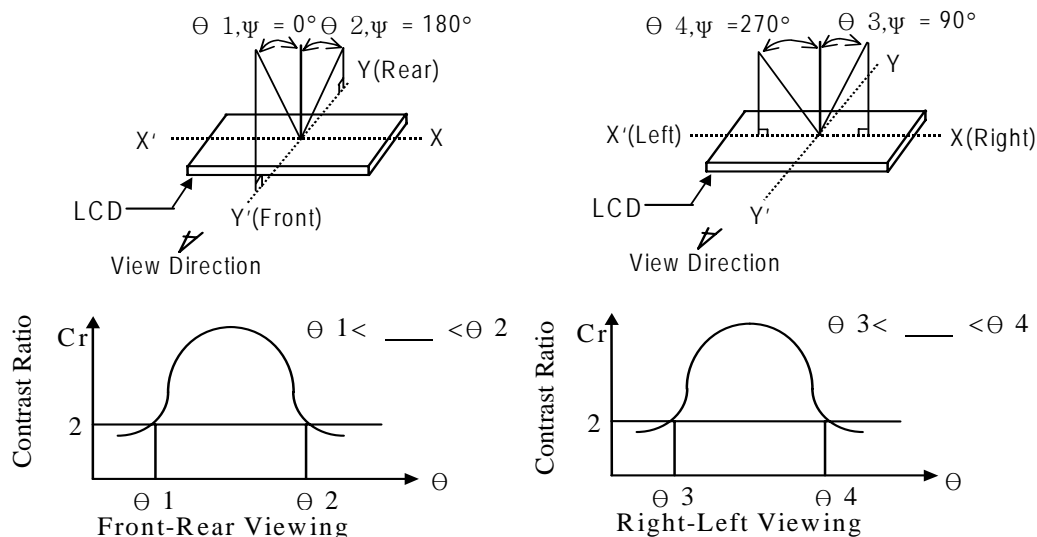
130

180

mA

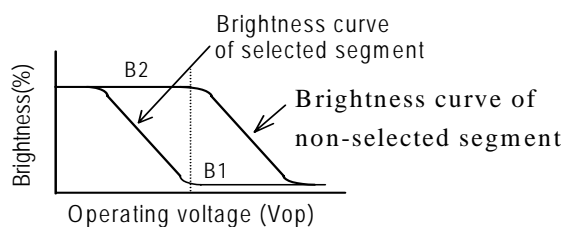
6.OPTICAL CHARACTERISTICS

(1) DEFINITION OF VIEWING ANGLE

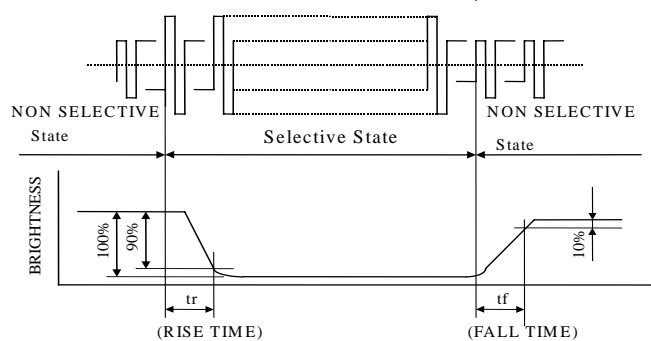


(2) DEFINITION OF CONTRAST

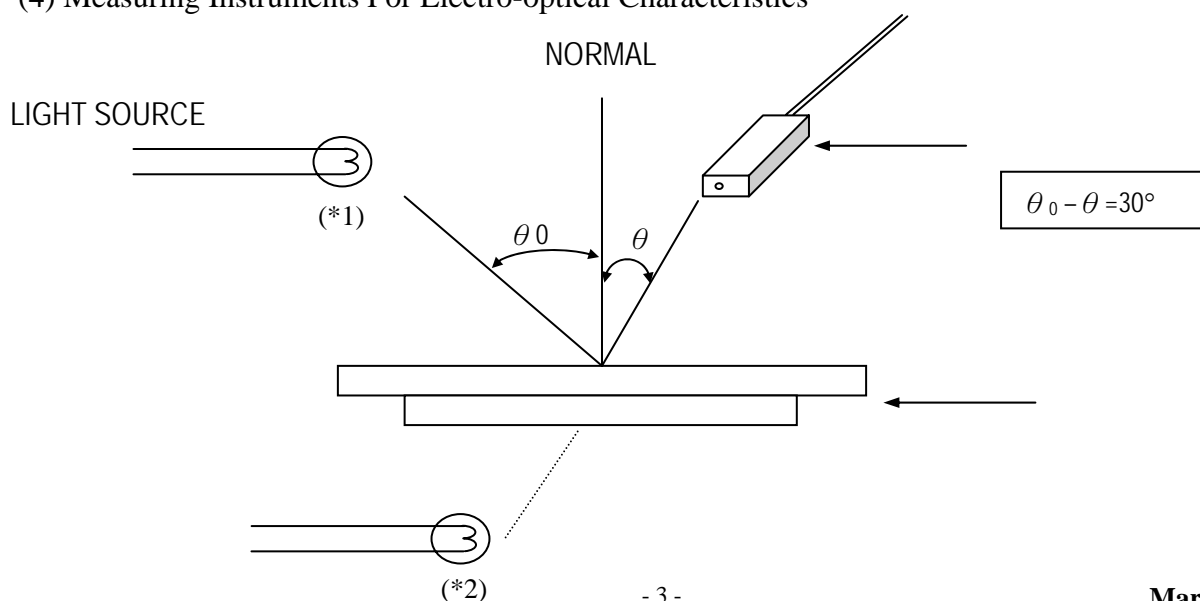
$$C.R = \frac{\text{Brightness of non-selected segment (B2)}}{\text{Brightness of selected segment (B1)}}$$



(3) DEFINITION OF RESPONSE



(4) Measuring Instruments For Electro-optical Characteristics



7.0 .TIMING CHARACTERISTICS

RA8802 LCD controller is the same with others, supporting both 8080 and 6800 Series. The pin of SYS_MI is for CPU type selection. It's active on reset period. Pull high when 6800 MPU is used. Pull low when 8080 MPU series are used.

7.1 Interface of 8080 Series

Please refer to Figure 7-1 when 8080 MCU Series is used.

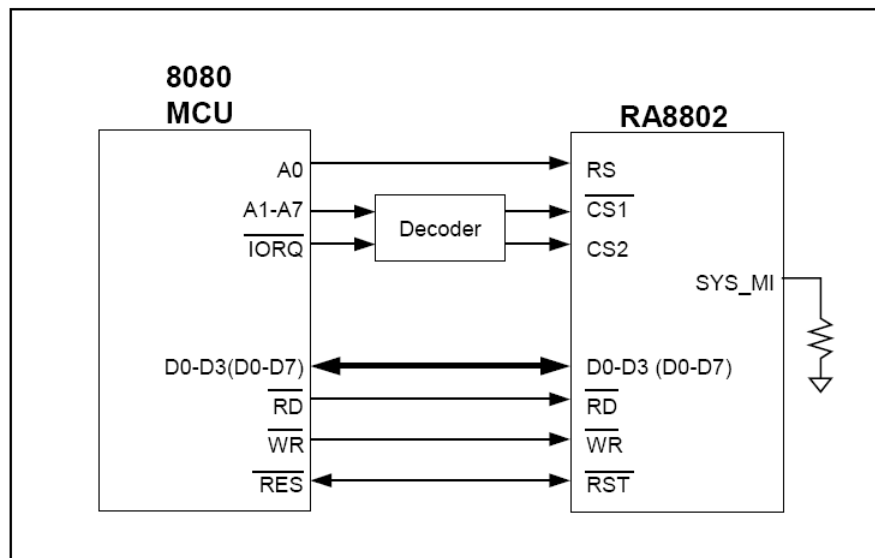


Figure 7-1:The diagram of 8080(4/8-bit) MCU and RA8802

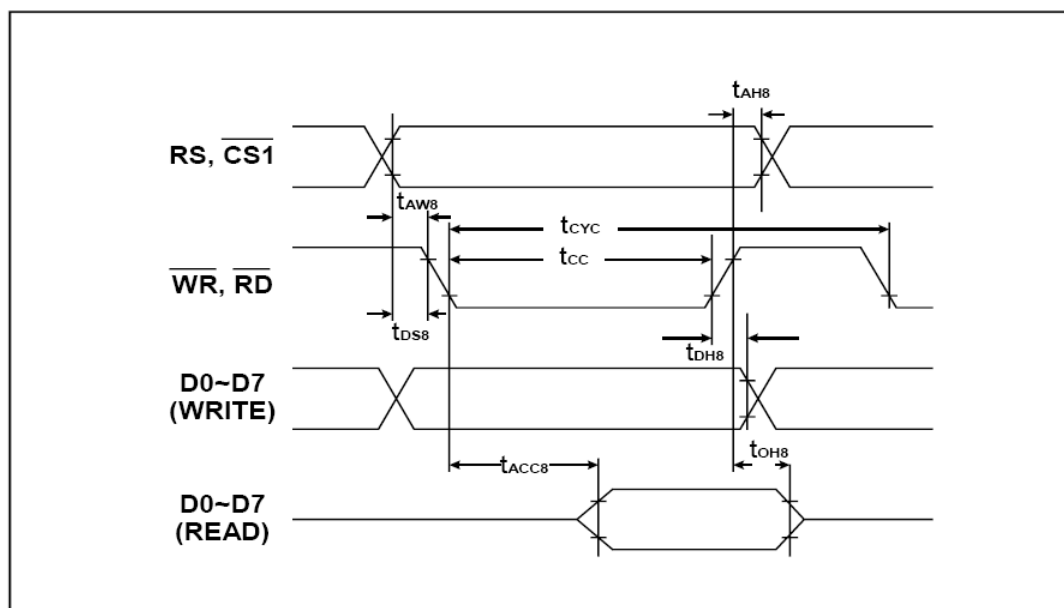


Figure 7-2:8-bit MCU access RA8802 Register

Signal	Symbol	Parameter	Rating		Unit	Condition
			Min	Max		
RS, CS1#	t_{AH8}	Address hold time	10	--	ns	System Clock: 8MHz Voltage: 3.3V
	t_{Aw8}	Address setup time	63	--	ns	
WR#, RD#	t_{CYC}	System cycle time	800	--	ns	
	t_{CC}	Strobe pulse width	400	--	ns	
D0 to D7	t_{DS8}	Data setup time	63	--	ns	
	t_{DH8}	Data hold time	10	--	ns	
	t_{ACC8}	RD access time	--	330	ns	
	t_{OH8}	Output disable time	10	--	ns	

7.2 MCU Interface of 6800 Series

Figure 7-3 is the MCU I/F Diagram of RA8802 and 6800Series. The Read/Write of 6800 MCU are the same pin. When RD/WR is High, it is doing read activity; when RD/WR is low, it is doing write activity.

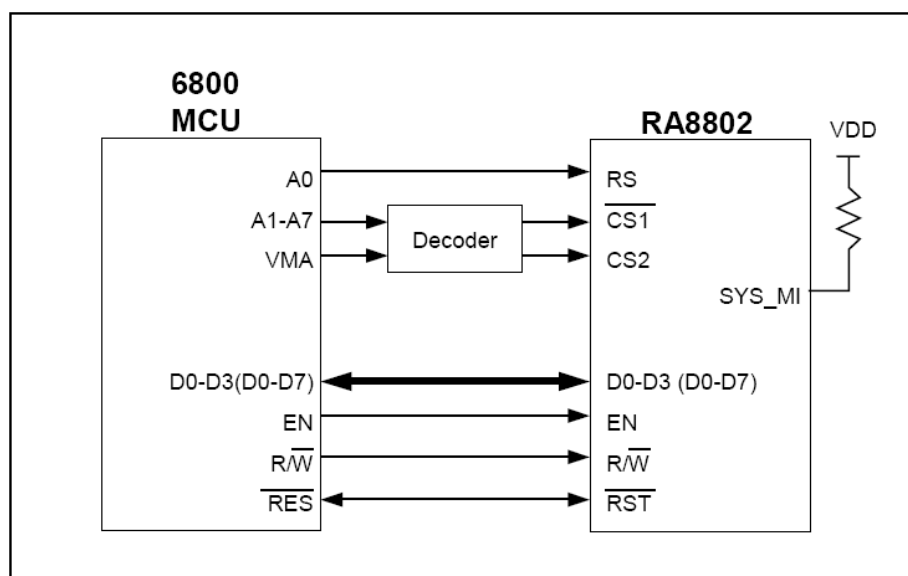


Figure 7-3: The Diagram of 6800(4/8-bit) MCU and RA8802

RA8802 couldn't accept signal from 6800 and 8080 at the same time; therefore, some pins will have different definition, such as, RD#(EN) (Pin#33). When users use 8080 MCU, then it is defined as RD#. But when users use 6800 MCU, then it is defined as EN. As for Pin #32, when users use 8080, then it is defined as WR#. However, when users choose 6800 MCU, then it is defined as RD/WR. You can refer to RA8802 Datasheet (Chapter4.1) for more details.

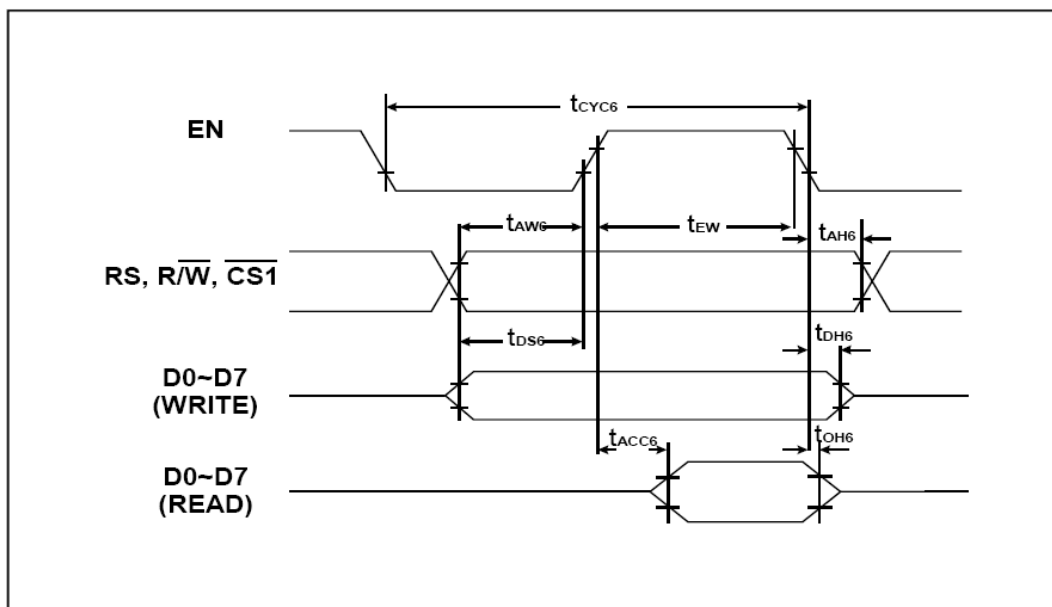


Figure 7-4:8-bit 6800 MCU access RA8802 Register

Signal	Symbol	Parameter	Rating		Unit	Condition
			Min	Max		
A0, R/W#, CS1#	t_{AH6}	Address hold time	10	--	ns	System Clock: 8MHz Voltage: 3.3V
	t_{AW6}	Address setup time	63	--	ns	
	t_{CYC6}	System cycle time	800	--	ns	
D0 to D7	t_{DS6}	Data setup time	63	--	ns	
	t_{DH6}	Data hold time	10	--	ns	
	t_{ACC6}	Access time	--	330	ns	
	t_{OH6}	Output disable time	10	--	ns	
EN	t_{EW}	Enable pulse width	400	--	ns	

7.3 MCU Interface 4Bit/8Bit

RA8802 could also support 4bit or 8bit MCU data bus. SYS_DB pin is for MPU data bit selection.

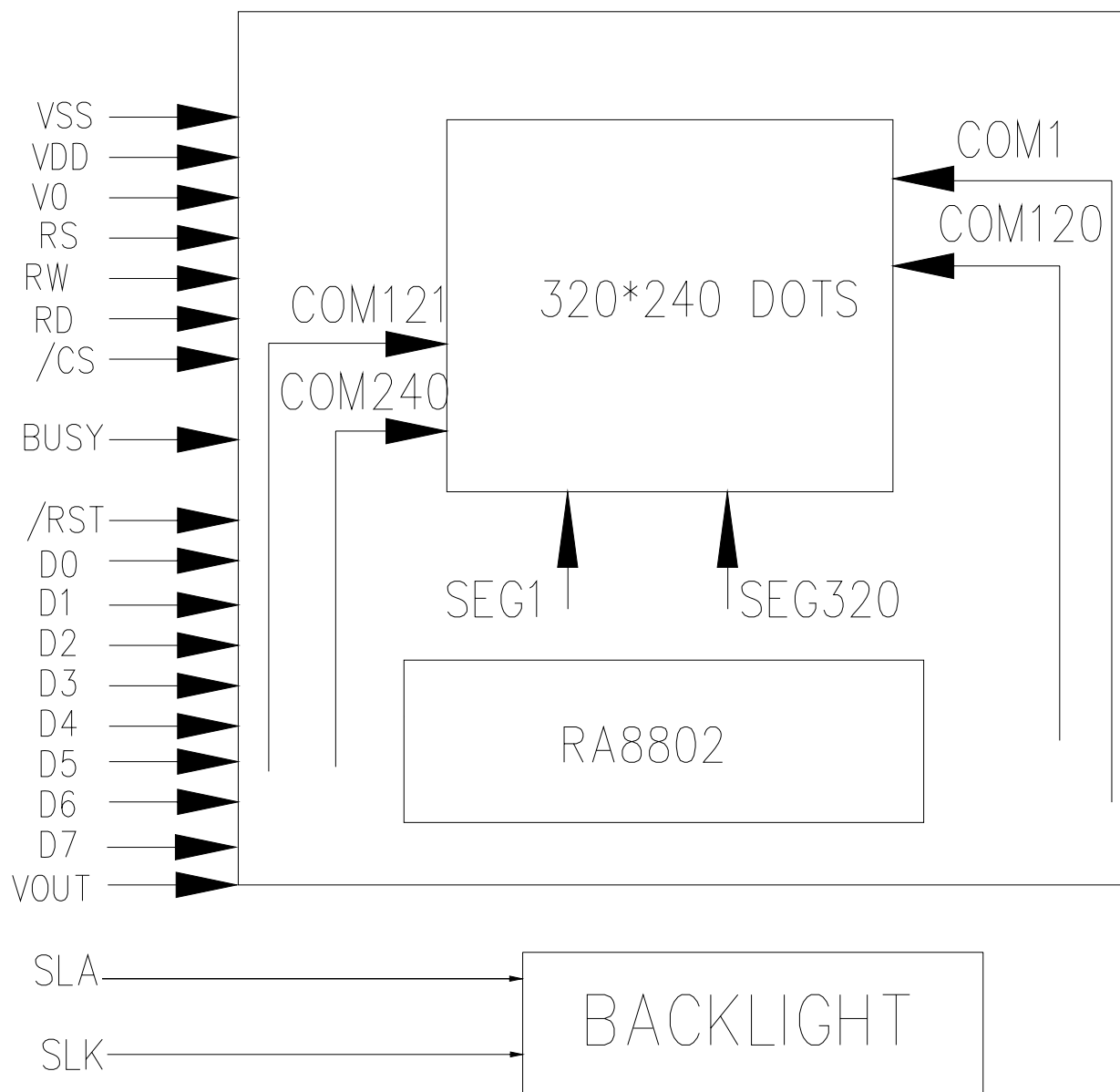
Pull high when 8-bit CPU is used. Pull low when 4-bit CPU is used. The high nibble data bus DB[7..4] should tied to GND When 4-bit CPU is used. Users could refer to example 5~8 in Chapter 2.4.

Because RA8802 internal register structure is 8-Bit structure, if users used 4-Bit data bus, then MCU need more Cycles to access Register.

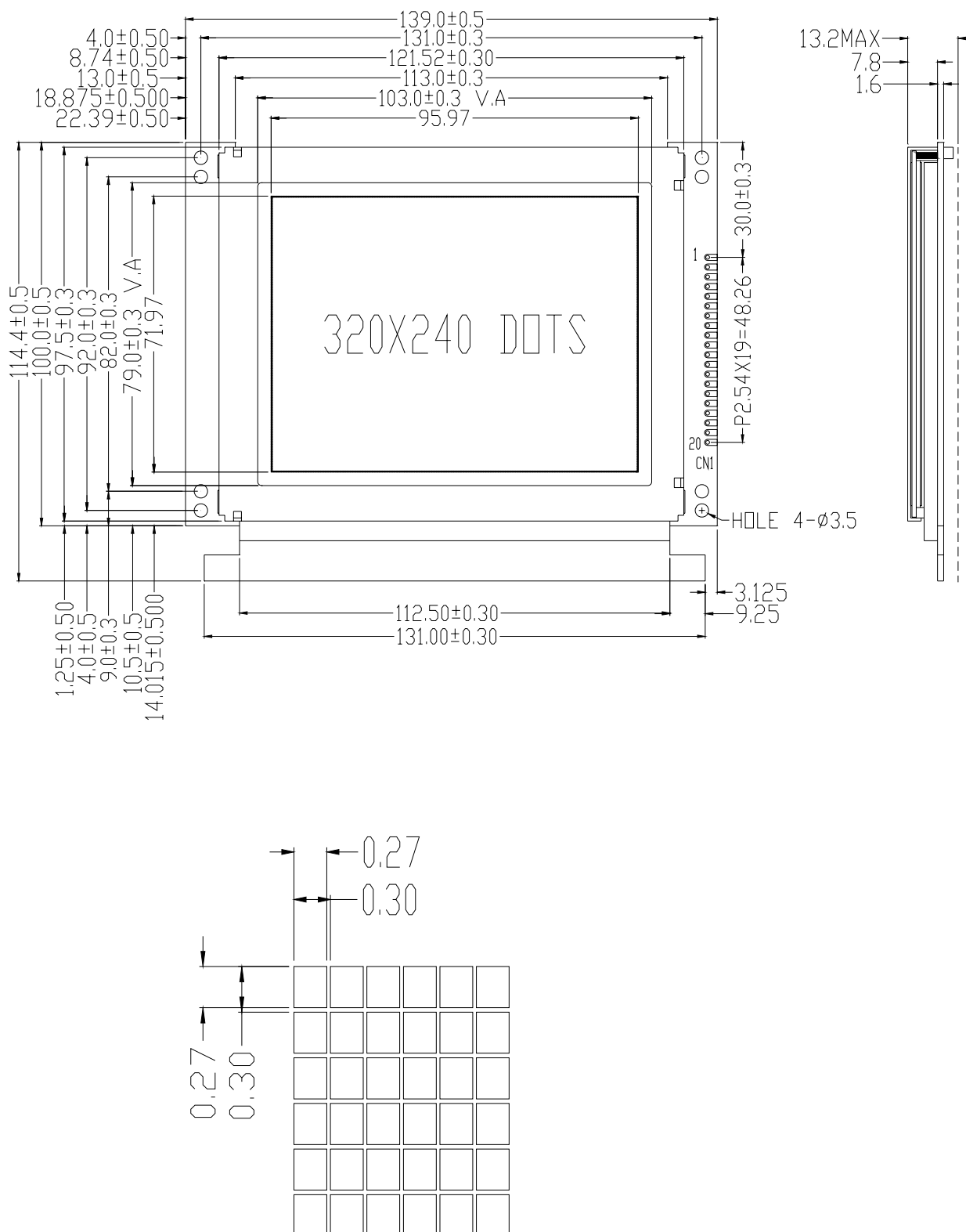
8. PIN ASSIGNMENT

Pin No.	Symbol	I/O	Description
1.	VSS	I	Ground
2.	VDD	I	Power supply for Logic(+5.0V)
3.	V0	I	Power supply for LCD Driver
4.	RS	I	Register/Data select pin Usually connect to MPU address bus A0. The MPU will access Instruction Register when RS is high and access Data Register when RS is low.
5.	WR	I	Active low WR signal when 8080 MCU is used. The data are latched at the rising edge of the WR signal.
6.	RD	I	Active low RD signal when 8080MCU is used.
7.	DB0	I/O	Data bit 0
8.	DB1	I/O	Data bit 1
9.	DB2	I/O	Data bit 2
10.	DB3	I/O	Data bit 3
11.	DB4	I/O	Data bit 4
12.	DB5	I/O	Data bit 5
13.	DB6	I/O	Data bit 6
14.	DB7	I/O	Data bit 7
15.	/RST	I	Reset signal
16.	/CS	I	Chip select pin. The RA8802 is active when CS1 is low and CS2 is high.
17.	BUSY	O	Busy signal.It should be connected to MCU I/O input.The MCU have to poll this pin before accessing RA8802.
18.	VOUT	O	DC-DC output voltage
19.	A	I	Backlight + terminal
20.	K	I	Backlight - terminal

9. BLOCK DIAGRAM



10.OUTLINE DIMENSIONS



11. RELIABILITY

Content of Reliability Test

Environmental Test				
No.	Test Item	Content of Test	Test Condition	Applicable Standard
1	High temperature storage	Endurance test applying the high storage temperature for a long time.	80 °C 200 hrs	
2	Low temperature storage	Endurance test applying the low storage temperature for a long time.	-30 °C 200 hrs	
3	High temperature operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70 °C 200 hrs	
4	Low temperature operation	Endurance test applying the electric stress under low temperature for a long time.	-20 °C 200 hrs	
5	High temperature Humidity storage	Endurance test applying the high temperature and high humidity storage for a long time.	50 °C , 90% RH 96 hrs	MIL-202E-103B JIS-C5023
6	High temperature Humidity operation	Endurance test applying the electric stress (Voltage & Current) and temperature humidity stress to the element for a long time.	50 °C , 90% RH 96 hrs	MIL-202E-103B JIS-C5023
7	Temperature cycle	Endurance test applying the low and high temperature cycle. <div style="text-align: center;"> $\begin{array}{ccccc} -20^{\circ}\text{C} & & 25^{\circ}\text{C} & & 70^{\circ}\text{C} \\ \leftarrow 30\text{min.} \rightleftharpoons 5\text{min.} \rightleftharpoons 30\text{min.} \rightarrow \\ \text{1 cycle} \end{array}$ </div>	-20°C – 70°C 10 cycles	
Mechanical Test				
8	Vibration test	Endurance test applying the vibration during transportation and using.	10-22Hz → 1.5mmp-p 22-500Hz → 1.5G Total 0.5hrs	MIL-202E-201A JIS-C5025 JIS-C7022-A-10
9	Shock test	Constructional and mechanical endurance test applying the shock during transportation.	50G half sign wave 11 msdc 3 times of each direction	MIL-202E-213B

10	Atmospheric pressure test	Endurance test applying the atmospheric pressure during transportation by air.	115 mbar 40 hrs	MIL-202E-105C
Others				
11	Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V, RS=1.5 k CS=100 pF 1 time	MIL-883B-3015.1

Supply voltage for logic system = 3V. Supply voltage for LCD system = Operating voltage at 25°C.

12. QUALITY GUARANTEE

Acceptable Quality Level

Each lot should satisfy the quality level defined as follows.

- Inspection method : MIL-STD-105E LEVEL II Normal one time sampling
- AQL

Partition	AQL	Definition
A: Major	0.4%	Functional defective as product
B: Minor	1.5%	Satisfy all functions as product but not satisfy cosmetic standard

Definition of 'LOT'

One lot means the delivery quantity to customer at one time.

Conditions of Cosmetic Inspection

Environmental condition

The inspection should be performed at the 1cm of height from the LCD module under 2 pieces of 40W white fluorescent lamps (Normal temperature 20~25°C and normal humidity $60 \pm 15\%RH$).

Inspection method

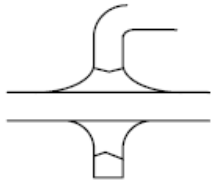

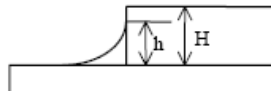
The visual check should be performed vertically at more than 30cm distance from the LCD panel.

Driving voltage

The VO value which the most optimal contrast can be obtained near the specified VO in the specification. (Within $\pm 0.5V$ of typical value at 25°C.).

13. INSPECTION CRITERIA

13.1 Module Cosmetic Criteria

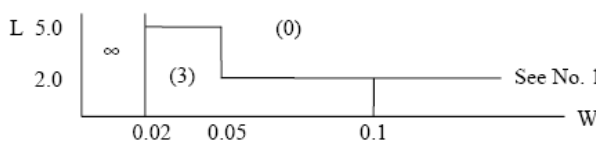
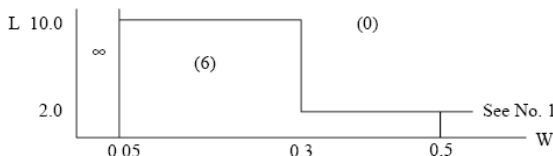
No.	Item	Judgement Criterion	Partition
1	Difference in Spec.	None allowed	Major
2	Pattern peeling	No substrate pattern peeling and floating	Major
3	Soldering defects	No soldering missing No soldering bridge No cold soldering	Major Major Major
4	Resist flaw on substrate	Invisible copper foil ('0.5mm or more) on substrate pattern	Minor
5	Accretion of metallic Foreign matter	No soldering dust No accretion of metallic foreign matters (Not exceed '0.2mm)	Minor Minor
6	Stain	No stain to spoil cosmetic badly	Minor
7	Plate discoloring	No plate fading, rusting and discoloring	Minor
8	Solder amount	<p>a. Soldering side of PCB Solder to form a 'Filet' all around the lead.</p>  <p>Solder should not hide the form perfectly. (too much)</p> <p>b. Components side (In case of 'Through Hole PCB')</p> <p>Solder to reach the Components side of PCB.</p>	Minor
	1. Lead parts		
	2. Flat packages	<p>Either 'Toe' (A) or 'Seal' (B) of the lead to be covered by 'Filet'.</p>  <p>Lead form to be assume over solder. A B</p>	Minor
	3. Chips	<p>$(3/2) H \geq h \geq (1/2) H$</p> 	Minor

13.2 Screen Cosmetic Criteria (Non-Operating)

No.	Defect	Judgement Criterion	Partition										
1	Spots	In accordance with <i>Screen Cosmetic Criteria (Operating) No.1.</i>	Minor										
2	Lines	In accordance with <i>Screen Cosmetic Criteria (Operating) No.2.</i>	Minor										
3	Bubbles in polarizer	<table><tr><th>Size : d mm</th><th>Acceptable Qty in active area</th></tr><tr><td>$d \leq 0.3$</td><td>Disregard</td></tr><tr><td>$0.3 < d \leq 1.0$</td><td>3</td></tr><tr><td>$1.0 < d \leq 1.5$</td><td>1</td></tr><tr><td>$1.5 < d$</td><td>0</td></tr></table>	Size : d mm	Acceptable Qty in active area	$d \leq 0.3$	Disregard	$0.3 < d \leq 1.0$	3	$1.0 < d \leq 1.5$	1	$1.5 < d$	0	Minor
Size : d mm	Acceptable Qty in active area												
$d \leq 0.3$	Disregard												
$0.3 < d \leq 1.0$	3												
$1.0 < d \leq 1.5$	1												
$1.5 < d$	0												
4	Scratch	In accordance with spots and lines operating cosmetic criteria. When the light reflects on the panel surface, the scratches are not to be remarkable.	Minor										

5	Allowable density	Above defects should be separated more than 30mm each other.	Minor
6	Coloration	Not to be noticeable coloration in the viewing area of the LCD panels. Back-lit type should be judged with back-lit on state only.	Minor
7	Contamination	Not to be noticeable.	Minor

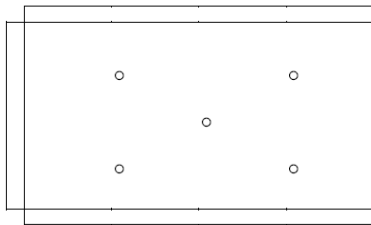
13.3. Screen Cosmetic Criteria (Operating)

No.	Defect	Judgement Criterion	Partition																				
1	Spots	<div>A) Clear Note :</div> <table><tr><th>Size : d mm</th><th>Acceptable Qty in active area</th></tr><tr><td>d ≤ 0.1</td><td>Disregard</td></tr><tr><td>0.1 < d ≤ 0.2</td><td>3</td></tr><tr><td>0.2 < d ≤ 0.3</td><td>2</td></tr><tr><td>0.3 < d</td><td>0</td></tr></table> <div>Including pin holes and defective dots which must be within one pixel size.</div> <div>B) Unclear Size :</div> <table><tr><th>Size : d mm</th><th>Acceptable Qty in active area</th></tr><tr><td>d ≤ 0.2</td><td>Disregard</td></tr><tr><td>0.2 < d ≤ 0.5</td><td>6</td></tr><tr><td>0.5 < d ≤ 0.7</td><td>2</td></tr><tr><td>0.7 < d</td><td>0</td></tr></table>	Size : d mm	Acceptable Qty in active area	d ≤ 0.1	Disregard	0.1 < d ≤ 0.2	3	0.2 < d ≤ 0.3	2	0.3 < d	0	Size : d mm	Acceptable Qty in active area	d ≤ 0.2	Disregard	0.2 < d ≤ 0.5	6	0.5 < d ≤ 0.7	2	0.7 < d	0	Minor
Size : d mm	Acceptable Qty in active area																						
d ≤ 0.1	Disregard																						
0.1 < d ≤ 0.2	3																						
0.2 < d ≤ 0.3	2																						
0.3 < d	0																						
Size : d mm	Acceptable Qty in active area																						
d ≤ 0.2	Disregard																						
0.2 < d ≤ 0.5	6																						
0.5 < d ≤ 0.7	2																						
0.7 < d	0																						
2	Lines	<div>A)Clear</div> <div></div> <div>Note : () - Acceptable Qty in active area L -Length (mm) W - Width (mm) ∞ - Disregard</div> <div>B) Unclear</div> <div></div>	Minor																				

'Clear' = The shade and size are not changed by VO.

'Unclear' = The shade and size are changed by VO.

13.4. Screen Cosmetic Criteria (Operating) (Continued)

No.	Defect	Judgement Criterion	Partition
3	Rubbing line	Not to be noticeable.	
4	Allowable density	Above defects should be separated more than 10mm each other.	Minor
5	Rainbow	Not to be noticeable.	Minor
6	Dot size	To be 95% ~ 105% of the dot size (Typ.) in drawing. Partial defects of each dot (ex. pin-hole) should be treated as 'Spot'. (see <i>Screen Cosmetic Criteria (Operating) No.1</i>)	Minor
7	Uneven brightness (only back-lit type module)	Uneven brightness must be $B_{MAX} / B_{MIN} \leq 2$ - B_{MAX} : Max. value by measure in 5 points - B_{MIN} : Min. value by measure in 5 points Divide active area into 4 vertically and horizontally. Measure 5 points shown in the following figure.  <small>○ : Measuring points</small>	Minor

Note :

- (1) Size : $d = (\text{long length} + \text{short length}) / 2$
- (2) The limit samples for each item have priority.
- (3) Complexed defects are defined item by item, but if the number of defects are defined in above table, the total number should not exceed 10.
- (4) In case of 'concentration', even the spots or the lines of 'disregarded' size should be not allowed. Following three situations should be treated as 'concentration'.
 - 7 or over defects in circle of '5mm.
 - 10 or over defects in circle of '10mm.
 - 20 or over defects in circle of '20mm.

14. PRECAUTIONS FOR USING LCD MODULES

Handling Precautions

- (1) The display panel is made of glass. Do not subject it to a mechanical shock by dropping it or impact.
- (2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- (3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- (4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- (5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents :

- Isopropyl alcohol
- Ethyl alcohol

(6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.

- Water
- Ketone
- Aromatic solvents

(7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.

(8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.

(9) Do not attempt to disassemble or process the LCD module.

(10) NC terminal should be open. Do not connect anything.

(11) If the logic circuit power is off, do not apply the input signals.

(12) To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

- Be sure to ground the body when handling the LCD modules.
- Tools required for assembling, such as soldering irons, must be properly grounded.
- To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.
- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

Storage Precautions

When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep the modules in bags (avoid high temperature, high humidity and low temperatures below 0 °C). Whenever possible, the LCD modules should be stored in the same conditions in which they were shipped from our company.

Others

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

- Exposed area of the printed circuit board.
- Terminal electrode sections.

15. USING LCD MODULES

Liquid Crystal Display Modules

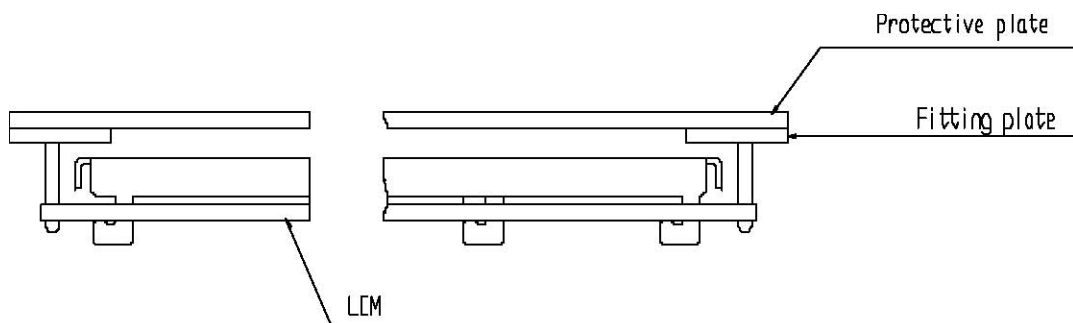
LCD is composed of glass and polarizer. Pay attention to the following items when handling.

- (1) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.
- (2) Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.).
- (3) N-hexane is recommended for cleaning the adhesives used to attach front/rear polarizers and reflectors made of organic substances which will be damaged by chemicals such as acetone, toluene, ethanol and isopropylalcohol.
- (4) When the display surface becomes dusty, wipe gently with absorbent cotton or other soft material like chamois soaked in petroleum benzin. Do not scrub hard to avoid damaging the display surface.
- (5) Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading.
- (6) Avoid contacting oil and fats.
- (7) Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizers. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- (8) Do not put or attach anything on the display area to avoid leaving marks on.
- (9) Do not touch the display with bare hands. This will stain the display area and degradate insulation between terminals (some cosmetics are determinated to the polarizers).
- (10) As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring.

Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.

- (1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



- (2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be 0.1mm.

Precaution for Handling LCD Modules

Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

- (1) Do not alter, modify or change the the shape of the tab on the metal frame.
- (2) Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
- (3) Do not damage or modify the pattern writing on the printed circuit board.
- (4) Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- (5) Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- (6) Do not drop, bend or twist LCM.

Electro-Static Discharge Control

Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC.

- (1) Make certain that you are grounded when handling LCM.
- (2) Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential.
- (3) When soldering the terminal of LCM, make certain the AC power source for the soldering iron does not leak.
- (4) When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- (5) As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.
- (6) To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%60% is recommended.

Precaution for soldering to the LCM

- (1) Observe the following when soldering lead wire, connector cable and etc. to the LCM.
 - Soldering iron temperature : 280 C 10 C.
 - Soldering time : 3-4 sec.
 - Solder : eutectic solder.

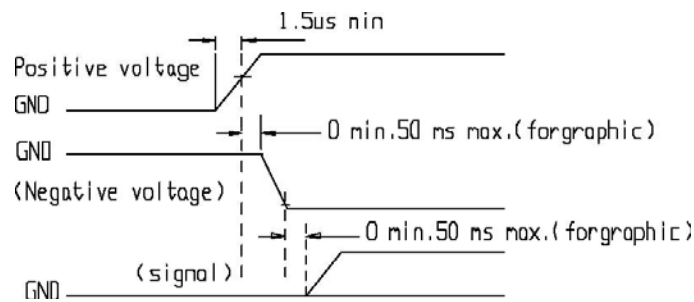
If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation. (This does not apply in the case of a non-halogen type of flux.) It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage dur to flux spatters.

(2) When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.

(3) When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

Precautions for Operation

- (1) Viewing angle varies with the change of liquid crystal driving voltage (VO). Adjust VO to show the best contrast.
- (2) Driving the LCD in the voltage above the limit shortens its life.
- (3) Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.
- (4) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
- (5) Condensation on terminals can cause an electrochemical reaction disrupting the terminal circuit. Therefore, it must be used under the relative condition of 40 °C , 50% RH.
- (6) When turning the power on, input each signal after the positive/negative voltage becomes stable.



Storage

When storing LCDs as spares for some years, the following precaution are necessary.

- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0 °C and 35 °C.
- (3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the container in which they were shipped.)

Safety

- (1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- (2) If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

Return LCM under warranty

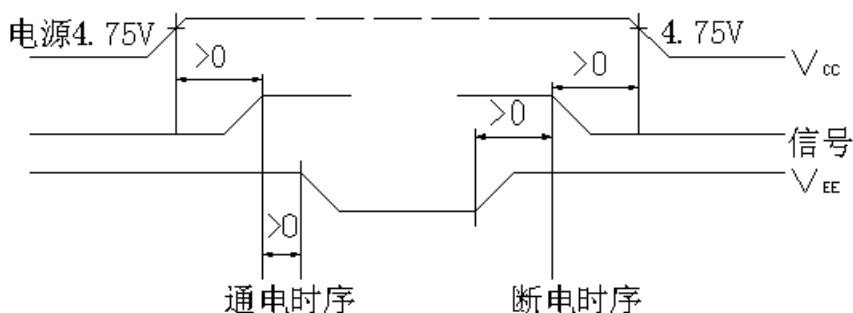
No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :

- Broken LCD glass.
- PCB eyelet's damaged or modified.
- PCB conductors damaged.
- Circuit modified in any way, including addition of components.
- PCB tampered with by grinding, engraving or painting varnish.
- soldering to or modifying the bezel in any manner.

Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet's, conductors and terminals.

液晶显示模块使用注意事项

1. 请勿随意自行加工、整修、拆卸。
2. 避免对液晶屏表面施加压力。
3. 不要用手随意去摸外引线、电路板上的电路及金属框。
4. 如必须直接接触时，应使人体与模块保持同一电位，或将人体良好接地。
5. 焊接使用的烙铁、操作用的电动改锥等工具必须良好接地，没漏电。
6. 严防各种静电。
7. 模块使用接入电源及断开电源时，必须按图时序进行。即必须在正电源（ $5 \pm 0.25V$ ）稳定接入后，才能输入信号电平。如在电源稳定接入前，或断开后就输入信号电平，将会损坏模块中的集成电路，使模块损坏。



8. 点阵模块在调节时，应调整 VEE 至最佳对比度、视角时为止。如果 VEE 调整过高，不仅会影响显示，还会缩短液晶的寿命。
9. 模块表面结雾时，不要通电工作，因为这将引起电极化学反应，产生断线。
10. 模块要存储在暗处（避阳光），温度在 $-10^{\circ}\text{C} \sim +35^{\circ}\text{C}$ ，湿度在 RH60%以上的地方。如能装入聚乙烯口袋（最好有防静电涂层）并将口封住最好。

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