MODEL NO. : TM070SDH01

ISSUED DATE: 2009-12-16

VERSION : Ver 2.2

□ Preliminary Specification
■ Final Product Specification

Customer:

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Approved by	Notes
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#### SHANGHAI TIANMA Confirmed:

Prepared by	Checked by	Approved by
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This technical specification is subjected to change without notice



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## **Record of Revision**

Rev	Issued Date	Description	Editor
1.0	2008-07-10	Preliminary Release	Enhao Li
1.1	2008-08-22	Change the Pin's definition of FPC and the length of B/L power cord	Enhao Li
1.2	2008-10-16	Add Block Diagram Update Power Consumption	Enhao Li
2.0	2008-11-12	Final Product Specification	Enhao Li
2.1	2008-11-19	Update Module Name From TS070SAATD01-00 To TM070SDH01	Enhao Li
2.2	2009-12-16	Revise Interface to RGB18 bits without TCON in page 4 Update Operating Life Time in page 9 Revise View Angles in page 15 Update Reliability Test Remarks in page 19	Xing Nie
		*	
	13.		
	<u> </u>		



## 1 General Specifications

Matching connector (BE P-TWD) P/N: AF 730L-A2G1T

	Feature	Spec	
	Size	7 inch	
	Resolution	800(RGB) X 600	
	Interface	RGB 18 bits without TCON	
	Color Depth	262k	
	Technology Type	a-Si	
Display Spec.	Pixel Pitch (mm)	0.176 X 0.176	
	Pixel Configuration	R.G.B. Vertical Stripe	
	Display Mode	TM with Normally White	
	Surface Treatment(Up Polarizer)	Anti-Glare(3H)	
	Viewing Direction	12 o'clock	
	Gray Scale Inversion Direction	6 o'clock	
	LCM (W x H x D) (mm)	155.50 X 118.70 X 5.70	
March autoni	Active Area(mm)	141.00 X 105.75	
Mechanical Characteristics	With /Without TSP	Without TSP	
	Weight (g)	183.22	
	LED Numbers	18 LEDs	

Note 1: Viewing direction for best image quality is different from TFT definition, there is a 180 degree shift.

Note 2: Requirements on Environmental Protection: Q/S0002

Note 3: LCM weight tolerance: +/- 5%



## 2 Input/Output Terminals

No	Symbol I/O Description						
1	POL	1/0	Polarity selection	Comment			
2	STVD	I/O	Vertical start pulse input when U/D= H	Note 1			
		1/0		Note 1			
3	OEV	l I	Output enable				
4	CKV		Vertical clock	N. 4			
5	STVU	I/O	Vertical start pulse input when U/D= L	Note 1			
6	GND	Р	Power ground				
7	EDGSL	l	Select rising edge or falling edge				
8	VCC	Р	Power supply for digital circuit				
9	V9	ı	Gamma voltage level 9				
10	VGL	Р	Gate OFF voltage				
11	V2		Gamma voltage level 2				
12	VGH	Р	Gate ON voltage				
13	V6	ı	Gamma voltage level 6				
14	U/D		Up/down selection				
15	VCOM		Common voltage				
16	GND	Р	Power ground				
17	AVDD	Р	Power supply for analog circuit				
18	V14		Gamma voltage level 14				
19	V11	ı	Gamma voltage level 11				
20	V8	i	Gamma voltage level 8				
21	V5	i	Gamma voltage level 5				
22	V3	i	Gamma voltage level 3				
23	GND	P	Power ground				
24	R5	<u>'</u>	Red data(MSB)				
25	R4		Red data				
26	R3	- '	Red data				
27							
	R2		Red data				
28	R1		Red data				
29	R0	<u> </u>	Red data(LSB)				
30	GND	Р	Power ground				
31	GND	P	Power ground				
32	G5	<u>l</u>	Green data(MSB)				
33	G4	ı	Green data				
34	G3	l	Green data				
35	G2		Green data				
36	G1	ı	Green data				
37	G0	l	Green data(LSB)				
38	STHL	I/O	Horizontal start pulse input when R/L = L	Note 1			
39	REV		Control signal are inverted or not				
40	GND	Р	Power ground				
41	DCLK		Sample clock				
42	VCC	Р	Power supply for digital circuit				
43	STHR	I/O	Horizontal start pulse input when R/L= H	Note 1			
44	LD	I	Latches the polarity of outputs and switches the new data to outputs				
45	B5	I	Blue data (MSB)				
40	CO	I	Diae agia (INIOD)				



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46	B4	I	Blue data	
47	B3	I	Blue data	
48	B2	I	Blue data	
49	B1	I	Blue data	
50	B0	I	Blue data (LSB)	
51	R/L	I	Right/ left selection	
52	V1	I	Gamma voltage level 1	
53	V4	I	Gamma voltage level 4	
54	V7	I	Gamma voltage level 7	
55	V10	I	Gamma voltage level 10	
56	V12		Gamma voltage level 12	
57	V13	I	Gamma voltage level 13	
58	AVDD	Р	Power supply for analog circuit	
59	GND	Р	Power ground	<b>P</b>
60	VCOM	Ī	Common voltage	

#### Note 1:

Set	Setting		Out State fo	r Start Pu	lse	Scanning Direction		
U/D	R/L	STVD	STVU	STHR	STHL	Scanning Direction		
GND	VCC	0	I		0	UP to down, left to right		
VCC	GND	I	0	0	I	Down to up, right to left		
GND	GND	0	I	0	I	UP to down, right to left		
VCC	VCC	I	0		0	Down to up, left to right		

Note 2: I/O definition:

I-----Input O---Output P----Power/Ground



## 3 Absolute Maximum Ratings

Ta = 25°C

Item	Symbol	Min	Max	Unit	Remark
Logic Supply Voltage	VCC	-0.3	5.0	V	
Analog Supply Voltage	AVDD	-0.3	15	V	
Positive power for scan driver	VGH	-0.3	42.0	V	
Negative power for scan driver	VGL	-20	0.3	V	
Voltage range of VGH- VGL	VGH- VGL	-0.3	40.0	V	
Back Light Forward Current	I <sub>LED</sub>		25	mΑ	For each LED
Gamma voltage	V1~V7	0.4 AVDD	AVDD+0.3	V	Note 1
Gamma voltage	V8~V14	-0.3	0.6 AVDD	V	Note 1
Operating Temperature	$T_{OPR}$	-20	70	$\mathbb{C}$	
Storage Temperature	$T_{STG}$	-30	80	$^{\circ}$	

Note 1: AVDD-0.1  $\geqslant$  V1  $\geqslant$  V2  $\geqslant$  V3  $\geqslant$  V4  $\geqslant$  V5  $\geqslant$  V6  $\geqslant$  V7  $\geqslant$  V8  $\geqslant$  V9  $\geqslant$  V10  $\geqslant$  V11  $\geqslant$  V12  $\geqslant$  V13  $\geqslant$  V14  $\geqslant$ GND+0.1



#### 4 Electrical Characteristics

### 4.1 Driving TFT LCD Panel

GND=0V, Ta=25°C

Ite	em	Symbol	Min	Тур	Max	Unit	Remark
Logic Supply Voltage		VCC	3.0	3.3	3.6	<b>V</b>	
	Supply age	AVDD	-	10.0	ı	<b>V</b>	
	oower for driver	VGH	14.5	15	15.5		
	power for driver	VGL	-10.5	-10	-9.5		
VC	OM	VCOM	-	4.05	-	V	
Input Signal	. 1 1 2/21		0	ı	0.2xVCC	V	R0~5,G0~5,B0~5,POL, STVD,OEV,CKV,STVU,
Voltage	High Level	$V_{IH}$	0.8xVCC	ı	VCC	٧	EDGSL,U/D,STHL,REV, DCLK,STHR,LD,R/L
Output	Low Level	$V_{OL}$	0	-	0.2xVCC	٧	STVD,STVU,STHL,STHR
Signal Voltage	Signal High Voltage Level		0.8xVCC	1	VCC	>	31 VD,31 VO,3111E,31111X
		$I_{GH}$	-	0.217	-	mΑ	
(Pane	(Panel+LSI)			0.542	-	mΑ	
Power Co	nsumption	I <sub>CC</sub>	-	3.577	-	mΑ	
		I <sub>AVDD</sub>		35.788	1	mΑ	

Note: It is important that VCC & VGL should be applied before VGH.

### 4.2 Gamma Correction Voltage

Parameter	Symbol	Min	Тур	Max	Unit	Remark
	V1	-	9.800	-	V	
	V2	-	9.704	-	V	
	V3	-	7.866	-	V	
	V4	-	7.365	-	V	
Gamma	V5	-	6.980	-	V	
Correction	V6	-	6.287	-	V	
reference	V7	-	5.450	-	V	
Voltage	V8	-	4.550	-	V	
V1~V14	V9	-	3.713	-	V	
	V10	-	3.020	-	V	
	V11	-	2.635	-	V	
	V12	-	2.134	-	V	
	V13	-	0.296	-	V	
	V14	-	0.200	-	V	



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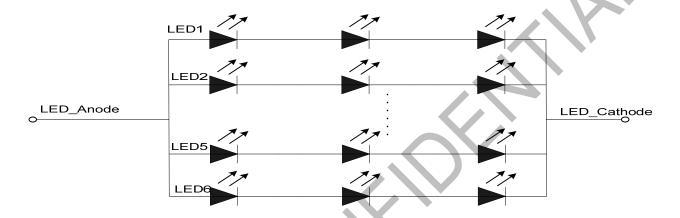
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4.3 Driving Backlight

Ta=25℃

Item	Symbol	Min	Тур	Max	Unit	Remark
Forward Current	I <sub>F</sub>	-	20	-	mA	Note 1
Forward Current Voltage	V <sub>F</sub>	-	9.6	-	V	Note 1
Backlight Power Consumption	W <sub>BL</sub>	-	1152	-	mW	Note 1
Operating Life Time		10000	(20000)		hrs	Note 3

Note 1: The LED driving condition is defined for all LED module (3 LED Serial, 6LED Parallel).



Note 2: One LED: IF =20 mA, VF =3.2V

Note 3: I<sub>F</sub> is defined for one channel LED.

Optical performance should be evaluated at Ta=25°C only.

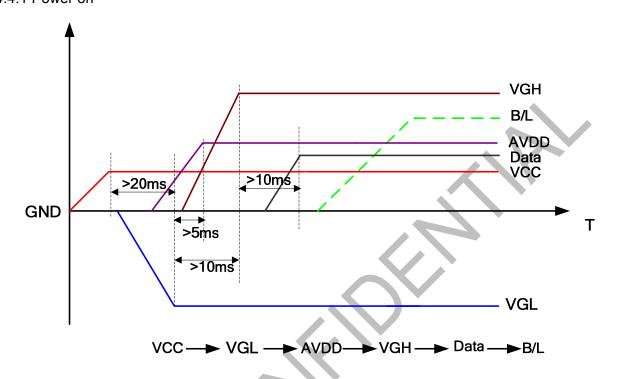
If LED is driven by high current, high ambient temperature & humidity condition.

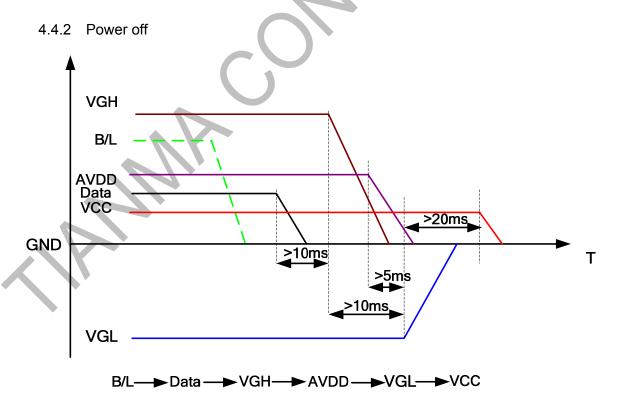
The life time of LED will be reduced.

Operating life means brightness goes down to 50% initial brightness.

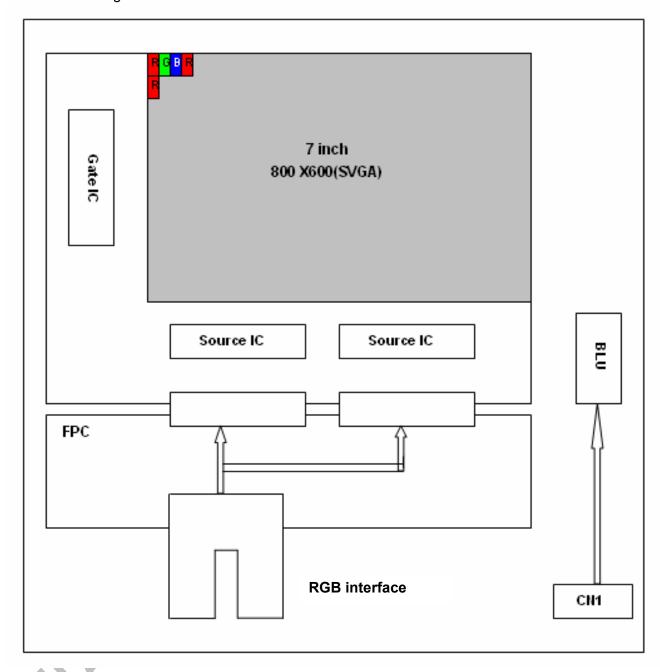
Typical operating life time is estimated data.

#### 4.4 Power Sequence 4.4.1 Power on





4.5 Block diagram





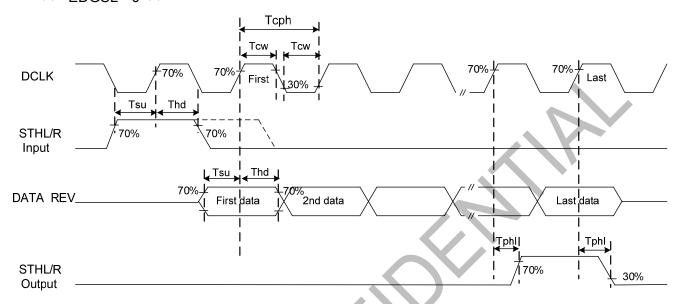
### Timing Chart

5.1.1. Clock and data input timing diagram1 (VCC=3.3V,AVDD=10.0V, GND=AGND=0V,Ta=25℃)

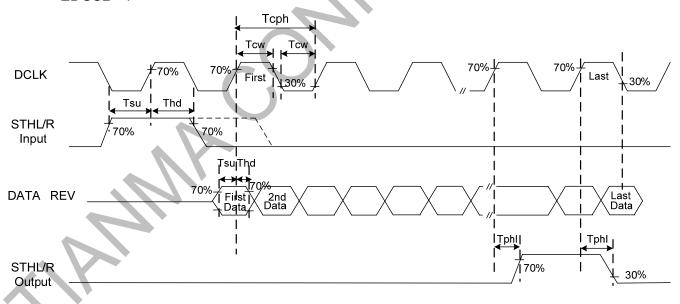
	11 (100 0.01,71722			: ::::: ; :::: : :::::::::::::::::::::		
Parameter	Symbol	Min	Тур	Max	Unit	Conditions
DCLK frequency	Fclk	-	50	55	MHz	EDGSL=0
DOLK frequency		-	25	27.5	MHz	EDGSL=1
DCLK cycle	Tcph	18	20	-	ns	EDGSL=0
		36	40	-	ns	EDGSL=1
DCLK pulse width	Tcw	40%	-	60%	Tcph	
Data set-up time	Tsu	4	-	-	ns	
Data hold time	Thd	2	-	-	ns	
Time that the last data to LD	Tld	1	-	ı	Tcph	
Pulse width of LD	Twld	2	-	ı	Tcph	
Time that LD to STHL/R	Tlds	5	-	-	Tcph	
POL set-up time	Tpsu	6	-	-	ns	POL to LD
POL hold time	Tphd	6	-		ns	POL to LD
Horizontal display timing range	Tdh	-	800	ı	Tcph	
Horizontal timing range	Th	-	1056	1	Tcph	
CKV pulse width	Pwclk	500	-	-	ns	High & Low
OEV pulse width	Twcl	1		-	us	
STVD/U set-up time	Tgsu	200	-	-	ns	
STVD/U hold time	Tghd	300	-	-	ns	
Horizonta lines per field	Tv	628	635	650	Tdh	
Vertical display timing range	Tvd	-	600	1	Tdh	

#### 5.1.2. Clock and data input timing diagram1 (VCC=3.3V,AVDD=10.0V, GND=AGND=0V,Ta=25℃)

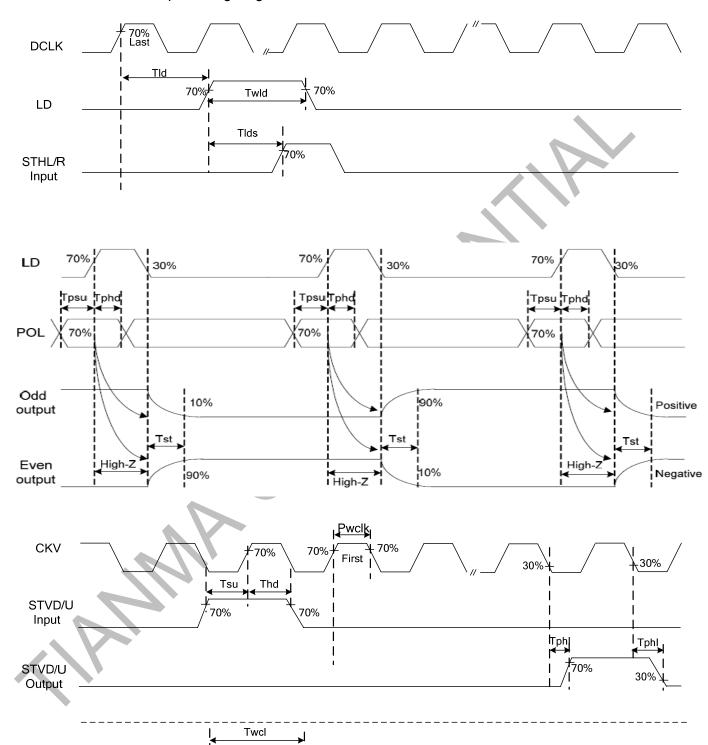
#### << EDGSL="0" >>



#### << EDGSL="1" >>



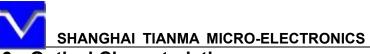
#### 5.1.3. Clock and data input timing diagram2



30%

70%

**OEV** 



## Optical Characteristics

### 6.1 Optical Specification

Ta=25°C

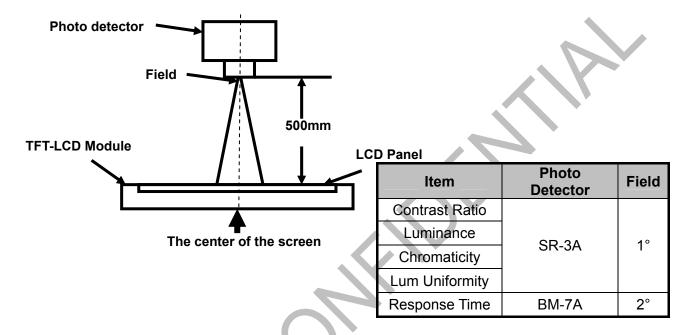
Item		Symbol	Condition	Min	Тур	Max	Unit	Remark
View Angles		θТ	-CR≧10	50	60	-	Degree	Note2,3
		θВ		60	70	-		
		θL		60	70			
		θR		60	70	-		
Contrast Ratio		CR	θ=0°	250	400	-		Note 3
Response Time		Ton	- <b>25</b> ℃	-	25	50	ms	Note 4
		Toff						
	White	Х	Backlight is	0.260	0.310	0.340		Note 1,5
		у		0.287	0.337	0.387		
	Red	х		0.536	0.568	0.636		
Chromaticity		у		0.297	0.347	0.397		
Omomaticity	Green	Х		0.286	0.336	0.386		
		у		0.524	0.574	0.624		
	Blue	Х		0.090	0.140	0.190		
		у		0.074	0.124	0.174		
Uniformity		U		70	80	-	%	Note 6
NTSC		-		45	50	-	%	Note 5
Luminance		1		160	200	-	cd/m <sup>2</sup>	Note 7

#### **Test Conditions:**

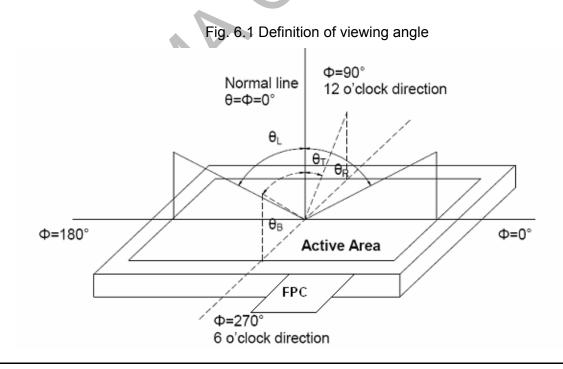
- 1. The ambient temperature is  $25^{\circ}$ C.
- 2. The test systems refer to Note 1 and Note 2.

Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Note 2: Definition of viewing angle range and measurement system. viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).



Note 3: Definition of contrast ratio

Contrast ratio (CR) =  $\frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$ 

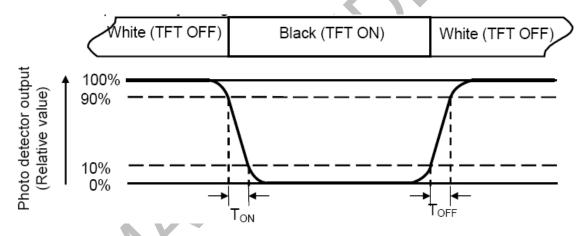
"White state ": The state is that the LCD should driven by Vwhite.

"Black state": The state is that the LCD should driven by Vblack.

Vwhite: To be determined Vblack: To be determined.

### Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

### Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity(U) = Lmin/Lmax

L----- Active area length W----- Active area width

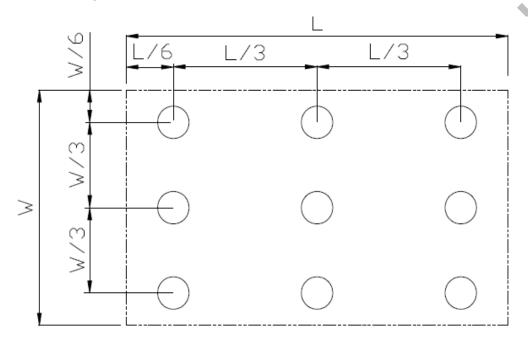


Fig. 6.2 Definition of uniformity

Lmax: The measured maximum luminance of all measurement position.

Lmin: The measured minimum luminance of all measurement position.

## Note 7: Definition of Luminance:

Measure the luminance of white state at center point.



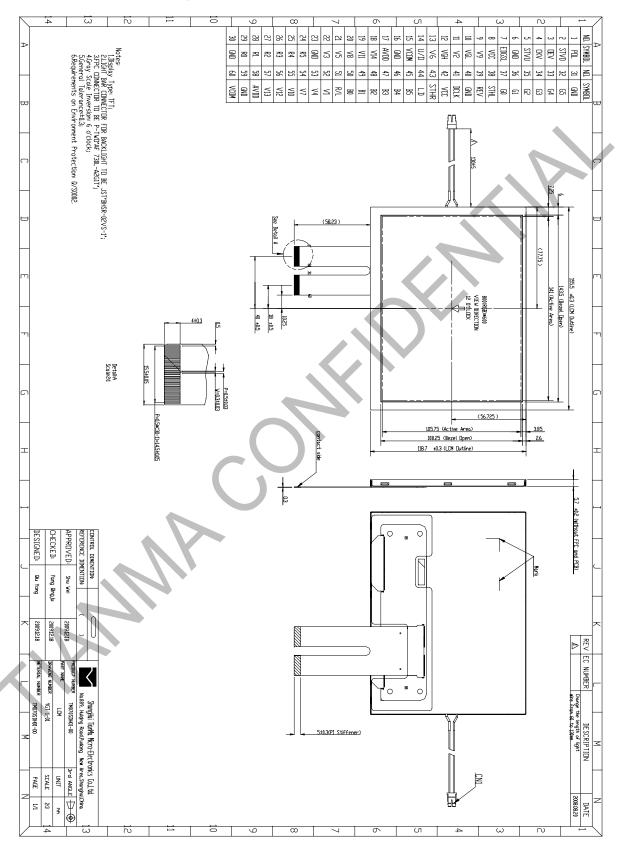
# 7 Environmental / Reliability Tests

No	Test Item	Condition	Remark
1	High Temperature Operation	Ts=+70℃, 240hrs	Note1 IEC60068-2-1,GB2423.2
2	Low Temperature Operation	Ta=-20℃, 240hrs	IEC60068-2-1 GB2423.1
3	High Temperature Storage (non-operation)	Ta=+80℃, 240hrs	IEC60068-2-1 GB2423.2
4	Low Temperature Storage (non-operation)	Ta=-30℃, 240hrs	IEC60068-2-1 GB2423.1
5	High Temperature & High Humidity Operation	Ta = +60℃,90% RH max,240 hours	Note2 IEC60068-2-78 GB/T2423.3
6	Thermal Shock (non-operation)	-30°C 30 min∼+80°C 30 min, Change time:5min, 100 Cycle	Start with cold temperature, end with high temperature IEC60068-2-14,GB2423.22
7	Electro Static Discharge (operation)	$\pm$ 2KV,Human Body Mode, 100pF/1500 $\Omega$	IEC61000-4-2 GB/T17626.2
8	Vibration (non-operation)	Sine Wave Frequency range:10~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z.(6 hours for total)	IEC60068-2-6 GB/T2423.10
9	Shock (non-operation)	100G 6ms, ±X,±Y,±Z 3times for each direction	IEC60068-2-27 GB/T2423.5
10	Package Drop Test	Height:80 cm,1 corner, 3 edges, 6 surfaces	IEC60068-2-32 GB/2423.8
11	Package Vibration Test	Random Vibration: 0.015G*G/Hz for 5-200Hz, -6dB/Octave from 200-500Hz 2 hours for each direction of X,Y,Z (6 hours for total)	IEC60068-2-34 GB/T2423.11

Note1: Ts is the temperature of panel's surface.

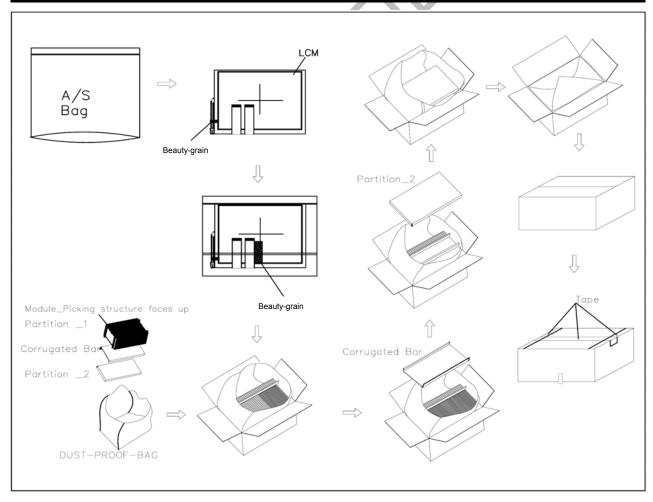
Note2: Ta is the ambient temperature of samples.

# **Mechanical Drawing**



## 9 Packing Drawing

No	Item	Model (Material)	Dimensions(mm)	Unit Weight(Kg)	Quantity	Remark
1	LCM module	TM070SDH01	155.5x118.7x5.70	0.18322	50	
2	Partition_1	Corrugated Paper	513x333x215	2.0	1	
٠.۲	Anti-Static Bag	PE	200x175	0.01	50	Anti-static
4	Dust-Proof Bag	PE	700x530	0.0600	1	
5	Partition_2	Corrugated Paper	505x332x4.00	0.1	2	
6	Corrugated Bar	Corrugated Paper	513x128x22.5	0.06	4	
7	Carton	Corrugated Paper	530x350x250	0.94	1	
8	Total weight(Kg)		13.00	)		





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#### 10 Precautions For Use of LCD Modules

- 10.1 Handling Precautions.
- 10.1.1. The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 10.1.2. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 10.1.3. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 10.1.4. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 10.1.5. If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
  - Isopropyl alcohol
  - Ethyl alcohol
- 10.1.6. Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:
  - Water
  - Ketone
  - Aromatic solvents
- 10.1.7. Do not attempt to disassemble the LCD Module.
- 10.1.8. If the logic circuit power is off, do not apply the input signals.
- 10.1.9. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
- 10.2 Be sure to ground the body when handling the LCD Modules.
- 10.3 Tools required for assembly, such as soldering irons, must be properly ground.
- 10.4 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
- 10.5 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.
- 10.6 Storage precautions
- 10.6.1. When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 10.6.2. The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:
- 10.7 Temperature : 0°C ~ 40°C Relatively humidity: ≤80%
- 10.7.1. The LCD modules should be stored in the room without acid, alkali and harmful gas.
- 10.7.2. Transportation Precautions
- 10.8 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.